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New on-chip communication architectures have been designed to support all inter-component communication in a SoC design. These communication architecture fabrics have a critical impact on the power consumption, performance, cost and design cycle time of modern SoC designs.

On-Chip Communication Architectures (System on Chip ...
On-Chip Communication Architectures: System on Chip Interconnect Sudeep Pasricha , Nikil Dutt Over the past decade, system-on-chip (SoC) designs have evolved to address the ever increasing complexity of applications, fueled by the era of digital convergence.

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K. Lahiri, A. Raghunathan and S. Dey, "System-level performance analysis for designing system-on-chip communication architecture," IEEE TCAD , Vol. 2, 2001, pp. 768-783. Google Scholar S. Pasricha and N. Dutt, "COSMECA: Application specific co-synthesis of memory and communication architectures for MPSoC," in Proceedings of Design, Automation and Test in Europe (DATE) , 2006, pp. 1-6.

On-Chip Communication Architectures | Guide books
System-on-chip (SoC) architectures are called to be the platform for an ever increasing number of interactive applications. One of the most time- consuming tasks is to define communication interfaces between the different components through a number

Unified inter-communication architecture for systems-on-chip
On-chip communication architectures can have a great influence on the speed and area of System-on-Chip designs, and this influence is expected to be even more pronounced on reconfigurable System ...

On-chip communication architectures for reconfigurable ...
Security is becoming the primary concern in today’s embedded systems. Network-on-chip (NoC)-based communication architectures have emerged as an alternative to shared bus mechanism in multi-core system-on-chip (SoC) devices and the increasing number and functionality of processing cores have made such systems vulnerable to security attacks.

Secure On-Chip Communication Architecture for ...
The ARM Advanced Microcontroller Bus Architecture (AMBA) is an open-standard, on-chip interconnect specification for the connection and management of functional blocks in system-on-a-chip (SoC) designs. It facilitates development of multi-processor designs with large numbers of controllers and components with a bus architecture.Since its inception, the scope of AMBA has, despite its name, gone ...

Advanced Microcontroller Bus Architecture - Wikipedia
A network on a chip or network-on-chip is a network-based communications subsystem on an integrated circuit, most typically between modules in a system on a chip. The modules on the IC are typically semiconductor IP cores schematizing various functions of the computer system, and are designed to be modular in the sense of network science. The network on chip is a router-based packet switching network between SoC modules. NoC technology applies the theory and methods of computer ...

Network on a chip - Wikipedia
interconnect is derived. This architecture is the result of a systematic architecture search and proper optimization based on the cost models. 2. Multi-conductor interconnects and on-chip antennas 2.1. Multi-conductorinterconnects With the increase of the on-chip data transfer rate to several 10 Gbit/s the spatio-temporal

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On-Chip Communication Architectures | ScienceDirect
Introduction System-on-chip (SOC) communication architecture is the fabric that integrates heterogeneous components and provides a mechanism for them to exchange data and control information. According to ITRS, ICs will have billions of transistors, with feature sizes around 50nm and clock frequencies around 10GHz in 2012. [1]

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Communication Architectures for Systems-on-Chip - 1st ...
Abstract: This paper presents a novel system-level performance analysis technique to support the design of custom communication architectures for system-on-chip integrated circuits. Our technique fills a gap in existing techniques for system-level performance analysis, which are either too slow to use in an iterative communication architecture design framework (e.g., simulation of the complete ...

System-level performance analysis for designing on-chip ...
Modern system-on-chip (SOC) designs consist of numerous heterogeneous components integrated onto a single chip (embedded CPUs, dedicated hardware, FPGAs, embedded memories, etc). The on-chip communication is becoming the bottleneck for these SOC designs most of which employ shared-bus based communication architecture.

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