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INTRODUCTION 1.1 47

2. 1.2 1.1 3. 1.3 4.

1.4 1.5 1.6 5. 1.7 6.

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wireless systems, low power integrated circuits, sensor networks, and

ubiquitous computing.

His current interests include the conception of the next-generation integrated wireless systems over a broad range of applications, as well as exploring the interaction between the cyber and the biological world.

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Table Of Contents

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Table of Contents

PREFACE PART I. THE

FOUNDATIONS

CHAPTER 1:

INTRODUCTION 1.1 A

Historical Perspective

1.2 Issues in Digital

Integrated Circuit

Design 1.3 Quality

Metrics of a Digital

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output resistance is
obtained when all
inputs are equal to
zero. Each of the pMOS
have the same width,
so all of them have the
same resistance. The
worst case resis- tance
happens when only

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one of the inputs (A, B,
C or D) is equal to 0

while all the rest are
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